

FIGURE 1

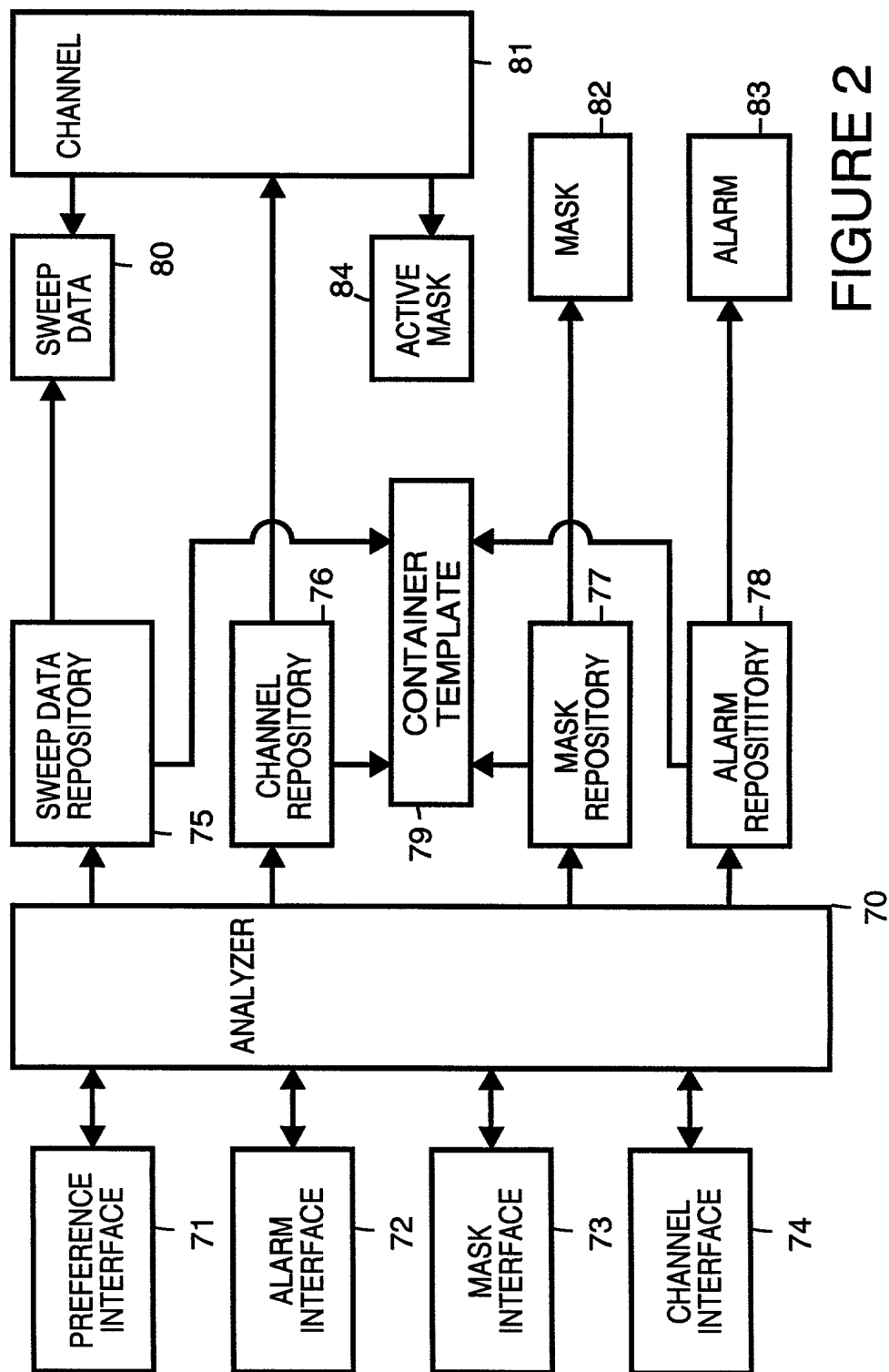


FIGURE 2

FIG. 3 is a schematic diagram of a prior art device. The device includes a substrate 31 and a series of conductive lines 32. The conductive lines 32 are arranged in a periodic pattern on the substrate 31.

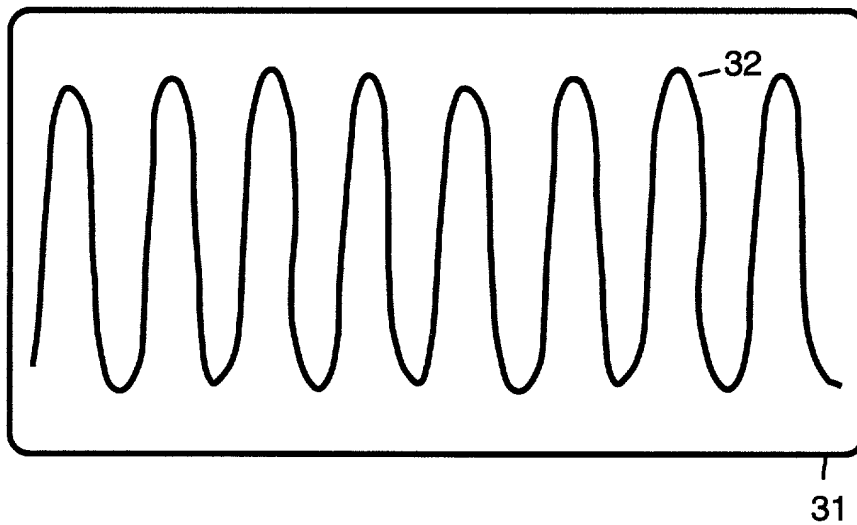


FIGURE 3 (PRIOR ART)

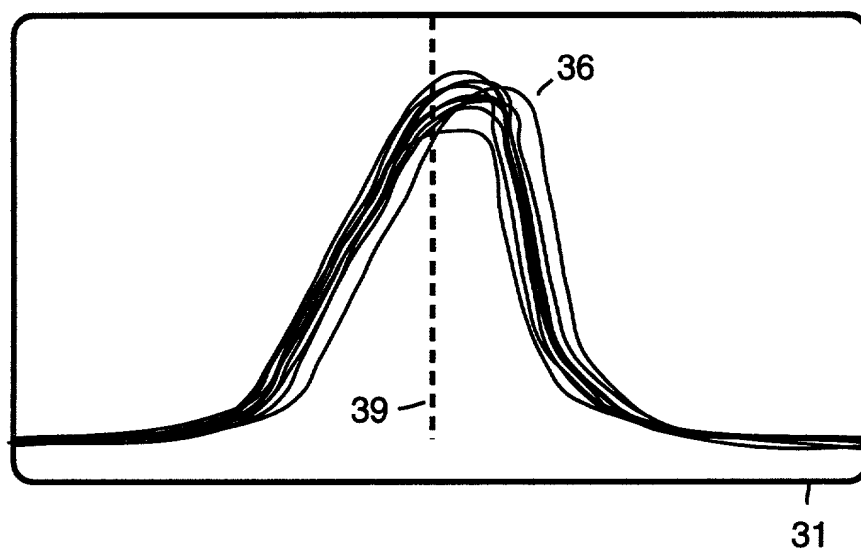


FIGURE 4

FIGURE 5

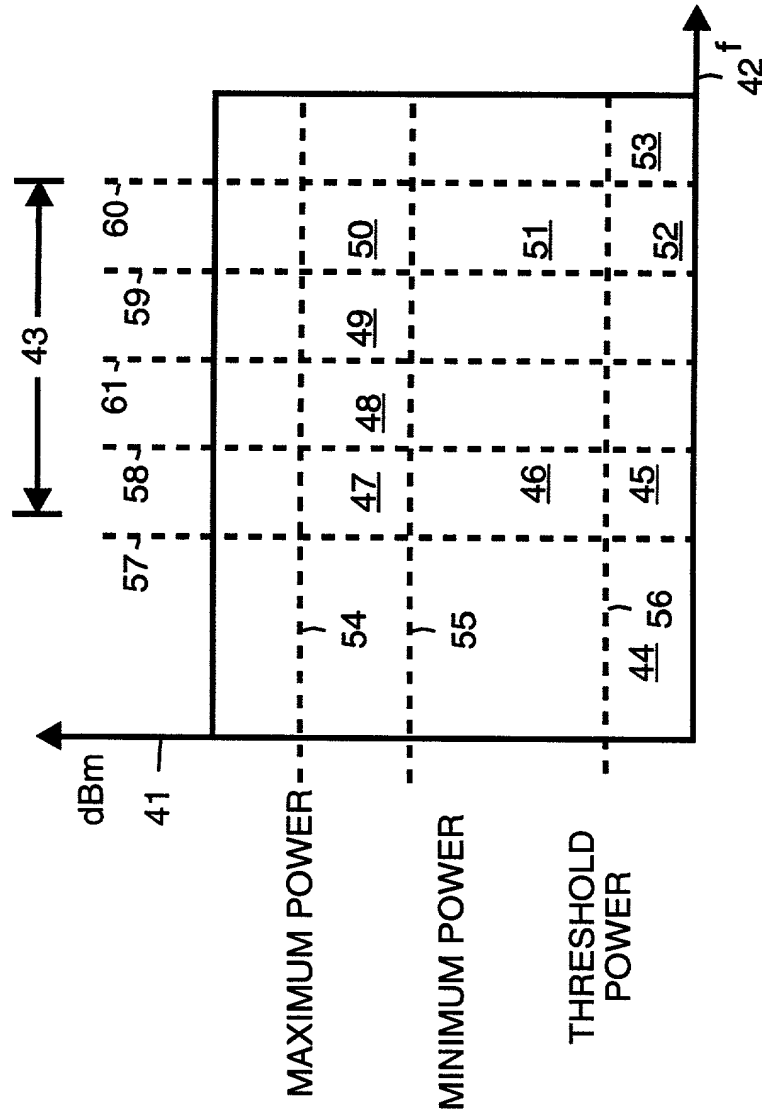


FIGURE 5

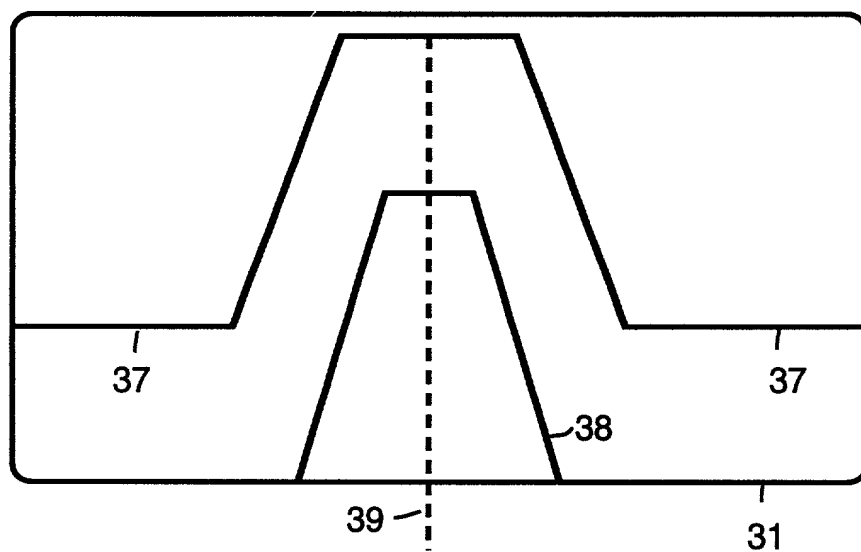


FIGURE 6

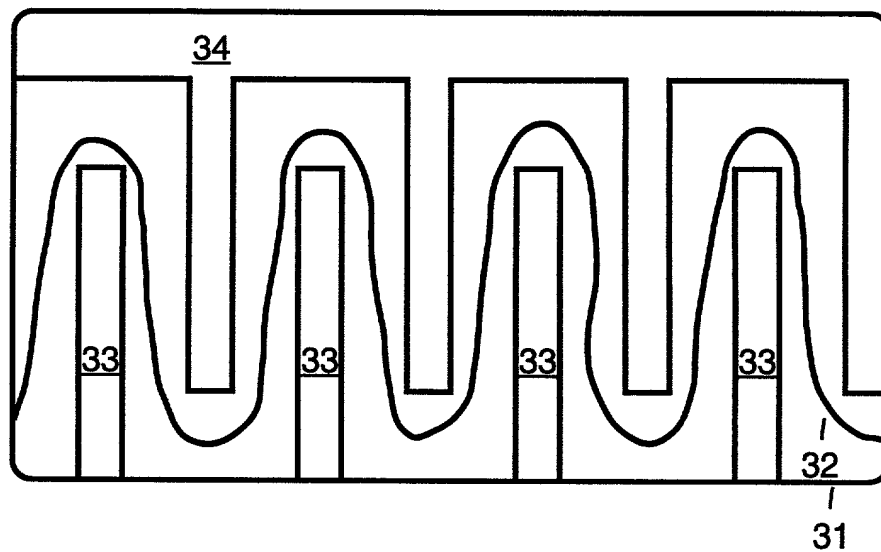


FIGURE 7

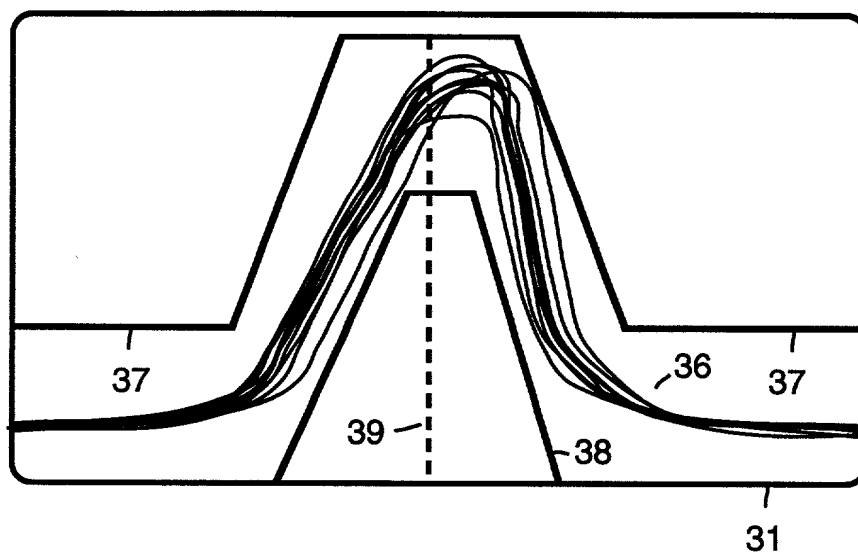


FIGURE 8